## Batched Second-Order Adjoint Sensitivity for Reduced Space Methods

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## Motivation: solving optimal power flow problems on GPU architectures

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## Challenge

Handling unstructured sparsity on SIMD architectures is non trivial

## Hardware <br> GPU centric (SIMD)



Physical model
unstructured



Compress the graph structure with a nonlinear reduction

$$
\begin{gathered}
\min _{\boldsymbol{x}, \boldsymbol{u}} f(\boldsymbol{x}, \boldsymbol{u}) \\
\text { subject to } g(\boldsymbol{x}, \boldsymbol{u})=0
\end{gathered}
$$

Why?

- The functional $\underline{x}(\boldsymbol{u})$ satisfies implicitly $g(\underline{x}(\boldsymbol{u}), \boldsymbol{u})=0$ !
- The second-order derivatives compress to a dense matrix


## Reduction method: formalism

We denote $\boldsymbol{x} \in \mathbb{R}^{n_{x}}$ the state, $\boldsymbol{u} \in \mathbb{R}^{n_{u}}$ the control

$$
\begin{gathered}
\min _{x, u} f(\boldsymbol{x}, \boldsymbol{u}) \\
\text { subject to } g(\boldsymbol{x}, \boldsymbol{u})=0
\end{gathered}
$$

Assumptions:

- Both the objective $f: \mathbb{R}^{n_{x}} \times \mathbb{R}^{n_{u}} \rightarrow \mathbb{R}$ and the physical equations $g: \mathbb{R}^{n_{x}} \times \mathbb{R}^{n_{u}} \rightarrow \mathbb{R}^{n_{x}}$ depend on $x$ and $u$
- Both $f$ and $g$ have smooth second-order derivatives


## Implicit function theorem

Let $(\boldsymbol{x}, \boldsymbol{u}) \in \mathbb{R}^{n_{x}} \times \mathbb{R}^{n_{u}}$ such that $g(\boldsymbol{x}, \boldsymbol{u})=0$.
If $\nabla_{\times} g(\boldsymbol{x}, \boldsymbol{u}) \in \mathbb{R}^{n_{x} \times n_{x}}$ is invertible, then there exists a local set $U \in \mathbb{R}^{n_{u}}$ and an unique differentiable function $\underline{x}: U \rightarrow \mathbb{R}^{n_{x}}$ such that locally

$$
g(\underline{x}(u), u)=0 \quad \forall u \in U
$$

In practice $\underline{x}(\boldsymbol{u})$ is computed iteratively (e.g. with a Newton Raphson algorithm)

## Reduced derivatives: first to second order

Let the reduced functional: $f_{r}(\boldsymbol{u}):=f(\underline{x}(\boldsymbol{u}), \boldsymbol{u})$
We note the Jacobians: $G_{x}=\nabla_{x} g \in \mathbb{R}^{n_{x} \times n_{x}}, G_{u}=\nabla_{u} g \in \mathbb{R}^{n_{x} \times n_{u}}$,

## Reduced gradient

The chain rule gives directly:

$$
\nabla f_{r}(\boldsymbol{u})=\nabla_{u} f-G_{u}^{\top} G_{x}^{-\top} \nabla_{x} f
$$

Complexity: one linear solve
For $\boldsymbol{\lambda} \in \mathbb{R}^{n_{x}}$, we define the Lagrangian

$$
\ell(x, u ; \lambda)=f(x, u)+\lambda^{\top} g(x, u)
$$

We note $W=\nabla^{2} \ell$ the Hessian of the Lagrangian in the full-space

## Reduced Hessian

The reduced Hessian satisfies

$$
\nabla^{2} f_{r}(u)=W_{u u}-W_{u x} G_{x}^{-1} G_{u}-G_{u}^{\top} G_{x}^{-\top} W_{x u}+G_{u}^{\top} G_{x}^{-\top} W_{x x} G_{x}^{-1} G_{u}
$$

Complexity: $n_{u}$ linear solves

## Implementation

## Reduction operation

$$
\nabla^{2} f_{r}(\boldsymbol{u})=\left[\begin{array}{c}
-G_{x}^{-1} G_{u} \\
\boldsymbol{l}
\end{array}\right]^{\top}\left[\begin{array}{ll}
W_{x x} & W_{x u} \\
W_{u x} & W_{u u}
\end{array}\right]\left[\begin{array}{c}
-G_{x}^{-1} \\
\boldsymbol{I}
\end{array}\right]
$$

Complexity: either

- $n_{u}$ linear solves if we store the $n_{x} \times n_{u}$ dense matrix $S=-G_{u} G_{x}^{-1}$
- $2 n_{u}$ linear solves otherwise

Two successive operations:

1. Evaluate the Hessian $W$ in the full space (automatic differentation)
2. Reduce the derivatives in the reduced space (linear algebra)

## How-to: efficient linear algebra operations on the GPU

- SpMV/SpMM (sparse matrix - vector/matrix product
- $\mathrm{SpSV} / \mathrm{SpSM}$ (sparse triangular solve)
- SpRF (sparse refactorization)


## First step: Streamlining the automatic differentation

## Factorizing the nonlinearities in the OPF problem (Lee et al., 2020)

There exists a basis $\psi(\boldsymbol{x}, \boldsymbol{u}) \in \mathbb{R}^{n_{b}}$ and two sparse matrices $M, N$ such that

$$
f(\boldsymbol{x}, u)=M \psi(x, u)+b, \quad g(x, u)=N \psi(x, u)+c,
$$

- Evaluate the basis $\psi$ first, then recover $f$ and $g$ with SpMV operations
- We have implemented a GPU kernel for $\psi$ and its adjoint $(\nabla \psi)^{\top}$


## Computing the Hessian with forward-over-reverse

The Hessian $W$ is a (super)-sparse matrix

- Find coloring associated to $W$
- Evaluate $W$ with forward-over-reverse, knowing

$$
(\nabla f)^{\top}=(\nabla \psi)^{\top} M^{\top}, \quad(\nabla g)^{\top}=(\nabla \psi)^{\top} N^{\top}
$$

Performance: with a dual vector $\boldsymbol{d} \in \mathbb{D}_{p}^{n}$ with $p$ partials, the linear operation $M^{\top} \boldsymbol{d}$ translates to a SpMM operation (RHS matrix with size $n \times(p+1)$ )

## First step: results

ExaPF: implemented in Julia, using CUDA and KernelAbstractions.jl

Our benchmark cases, ordered by size (obtained from MATPOWER):

| Case | $n_{v}$ | $n_{e}$ | $n_{x}$ | $n_{u}$ | $n_{\text {colors }}$ |
| :--- | ---: | ---: | ---: | ---: | :---: |
| IEEE118 | 118 | 186 | 181 | 107 | 27 |
| IEEE300 | 300 | 411 | 530 | 137 | 24 |
| PEGASE1354 | 1,354 | 1,991 | 2,447 | 519 | 28 |
| PEGASE2869 | 2,869 | 4,582 | 5,227 | 1,019 | 35 |
| PEGASE9241 | 9,241 | 16,049 | 17,036 | 2,889 | 85 |
| ACTIVSg25K | 25,000 | 32,230 | 47,246 | 6,531 | 36 |

## Protocol

Compare time to evaluate $W$ with

- Reference: JuMP's AD (open-source, Julia code)
- our algo on the CPU (ExaPF CPU)
- our algo on the GPU (ExaPF GPU)


## First step: results

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- Reference: JuMP's AD (open-source, Julia code)
- our algo on the CPU (ExaPF CPU)
- our algo on the GPU (ExaPF GPU)

| Case | JuMP (CPU) | ExaPF (CPU) | ExaPF (CUDA) |
| :--- | :--- | :--- | :--- |
| IEEE118 | 0.002 | 0.0002 | 0.003 |
| IEEE300 | 0.003 | 0.0005 | 0.003 |
| PEGASE1354 | 0.019 | 0.004 | 0.004 |
| PEGASE2869 | 0.043 | 0.013 | 0.006 |
| PEGASE9241 | 0.150 | 0.145 | 0.029 |
| ACTIVSg25K | 0.359 | 0.105 | 0.035 |

Table: Results: evaluation time in seconds

## Second step: reduction operation

We have computed the Hessian $W$ in the first step

$$
\nabla^{2} f_{r}(\boldsymbol{u})=\left[\begin{array}{c}
-G_{x}^{-1} G_{u} \\
\boldsymbol{l}
\end{array}\right]^{\top}\left[\begin{array}{ll}
W_{x x} & W_{x u} \\
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-G_{x}^{-1} G_{u} \\
\boldsymbol{I}
\end{array}\right]
$$

We should avoid allocating the sensitivity matrix $S=-G_{u} G_{X}^{-1}\left(\right.$ size $\left.n_{x} \times n_{u}\right)$ ! Instead, use batched HessMat product $\nabla^{2} f_{r}(u) V$

## HessMat kernel: batch adjoint-adjoint reduction

Input: LU factorization, such that $P G_{x} Q=L U$
(2 SpMM, 2 SpSM )
For every matrix $V \in \mathbb{R}^{n_{u} \times N}$

1. Solve $Z=G_{x}^{-1}\left(G_{u} V\right)$
(3 SpMM, 2 SpSM )
2. Evaluate $\left[\begin{array}{c}\Psi \\ H_{u}\end{array}\right]=\left[\begin{array}{ll}W_{x x} & W_{x u} \\ W_{u x} & W_{u u}\end{array}\right]\left[\begin{array}{l}Z \\ V\end{array}\right]$
3. Solve $H_{x}=G_{x}^{-\top} \Psi$
(2 SpMM, 2 SpSM )
4. Output $\nabla^{2} f_{r}(u) V=H_{u}-G_{u} H_{x}$

- $G_{x}$ first factorized on the CPU with KLU, then refactorized entirely on the GPU with cusolverRF (fast) ${ }^{1}$
- $\operatorname{div}\left(n_{u}, N\right)+1$ HessMat products required to get full $\nabla^{2} f_{r}(\boldsymbol{u})$

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## Second step: results

- Question 1: What is the appropriate batch size $N$ ?

- Question 2: What is the bottleneck in the reduction algorithm?



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- Question 1: What is the appropriate batch size $N$ ?

- Question 2: What is the bottleneck in the reduction algorithm?



## References I

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[^0]:    ${ }^{1}$ Credits to Kasia Swirydowicz, PNNL, for the idea and the RF wrapper (Świrydowicz et al., 2021)

